

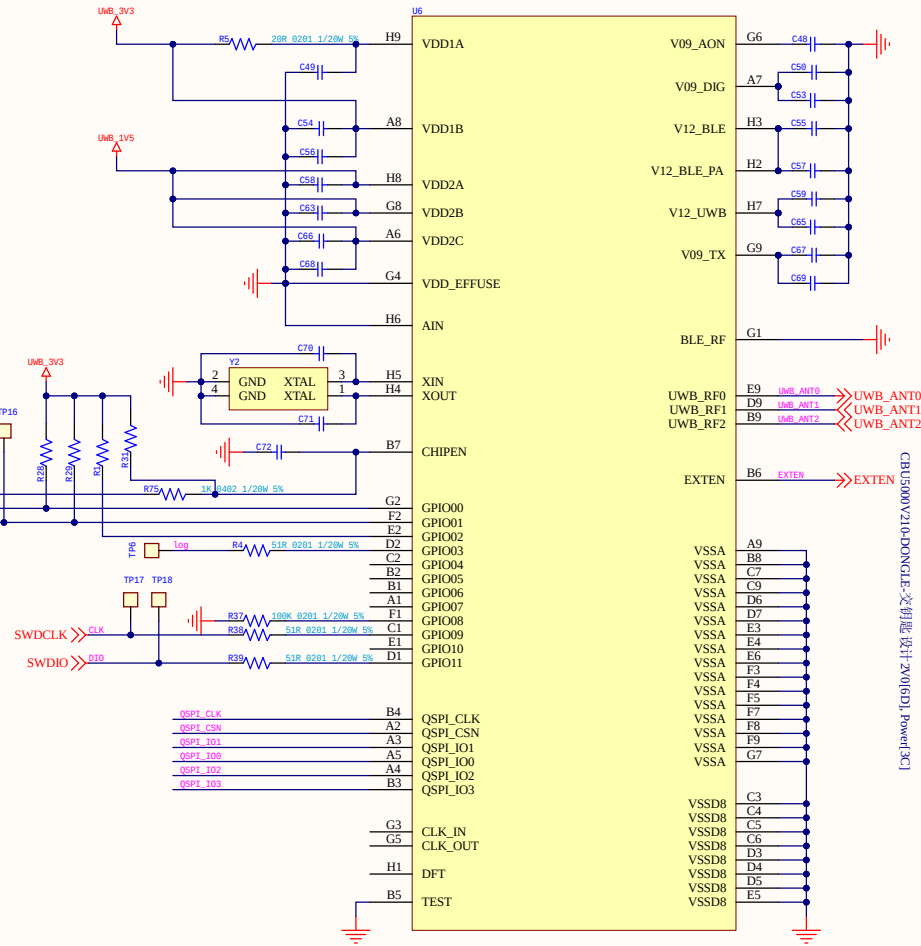
UWB

两路输入电源，3.3V和1.5V，IO口的电压跟随VDD1的电压

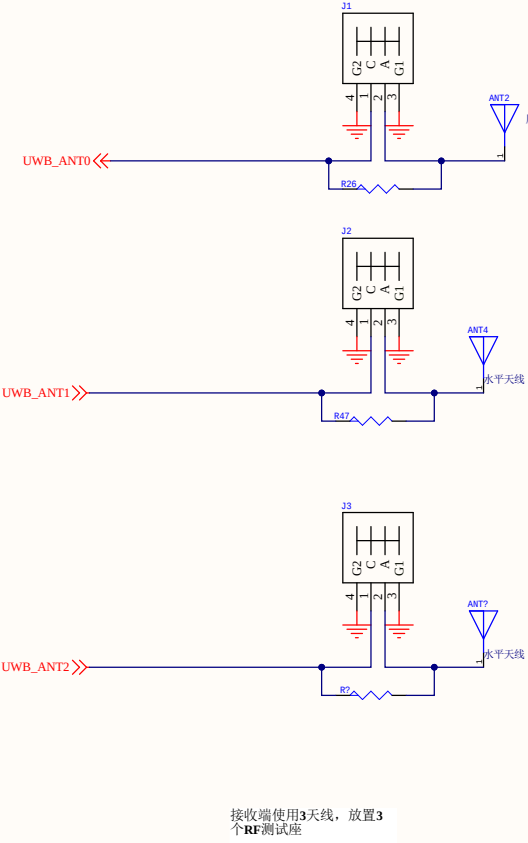
芯片的CHIPEN上拉时正常使用，下拉时芯片关闭，硬件默认上拉，可以用主控的MCU下拉控制

预留GPIO3为debug LOG口，GPIO6 7与MCU串口通信，GPIO9/11作为烧录口

芯片默认使用SWD模式，GPIO0,1,2上拉，GPIO8下拉为SWD模式，该模式下芯片可烧录

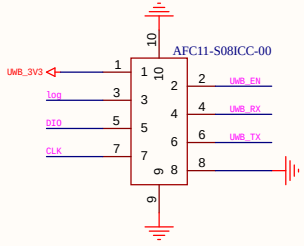


UWB RF

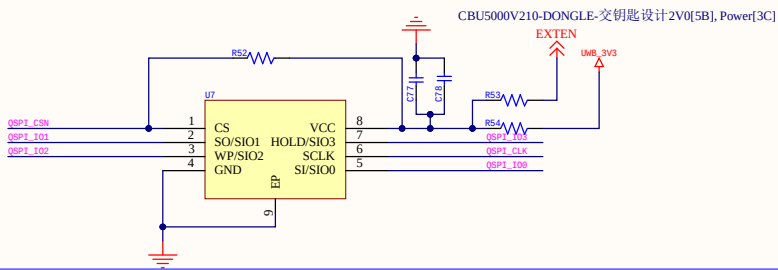


接收端使用3天线，放置3个RF测试座

FPC



FLASH



CompanyName:	*
Title:	CBU5000V210-DONGLE- 交钥匙设计2V0(1).SchDoc
DrawnBy:	*
Revision:	*
Date:	2026/3/5
Sheet:	1 of 2